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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,712,907 B1
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INVENTOR(S) : Pratt et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 7,

Line 42, after "acceleration", insert

-- ; and wherein the driven magnet array forms a magnetic rotation lock with the driving magnet array --

Column 8,

Line 13, after "acceleration", insert

-- ; and wherein the first magnet array forms a magnetic rotation lock with the second magnet array --

Lines 43-50, claim 19 should read,

-- A method for linearly translating a wafer in a semiconductor wafer fabrication system; comprising:

placing a wafer on a carriage, the carriage having a first magnet array;
magnetically coupling an actuator to the carriage using a second magnet array to
allow propulsion of the carriage, the actuator isolated from a vacuum environment;
forming a magnetic rotation lock between the first magnet array and the second
magnet array; and
translating the actuator linearly. --

Signed and Sealed this

Eighth Day of March, 2005



JON W. DUDAS
Director of the United States Patent and Trademark Office